

1

ASYNCHRONOUSLY-RESETTABLE DECODER WITH REDUNDANCY

A decoder providing asynchronous reset, redundancy, or both. 5
an asynchronously-resettable decoder with redundancy. The
decoder has a synchronous portion, responsive to a clocked
signal; an asynchronous portion coupled with an asynchronous
circuit; a feedback-resetting portion, which substantially
isolates the synchronous portion from the asynchronous portion
coupled with, and interposed between the synchronous portion in
10 response to a asynchronous reset signal; a signal input; a first
memory output coupled with a first memory cell group; a second
memory output coupled with a second memory cell group; and a
selector coupled between the signal input, the first memory
output, and the second memory output. This decoder can be memory
15 row-oriented, and thus provide an asynchronously-resettable row
decoder with row redundancy, or an asynchronously-resettable
column decoder with column redundancy.

20

JFO/vat

VAT IRV1042215.1--2/2/01 2:17 PM

25

30

35